

Figure 1

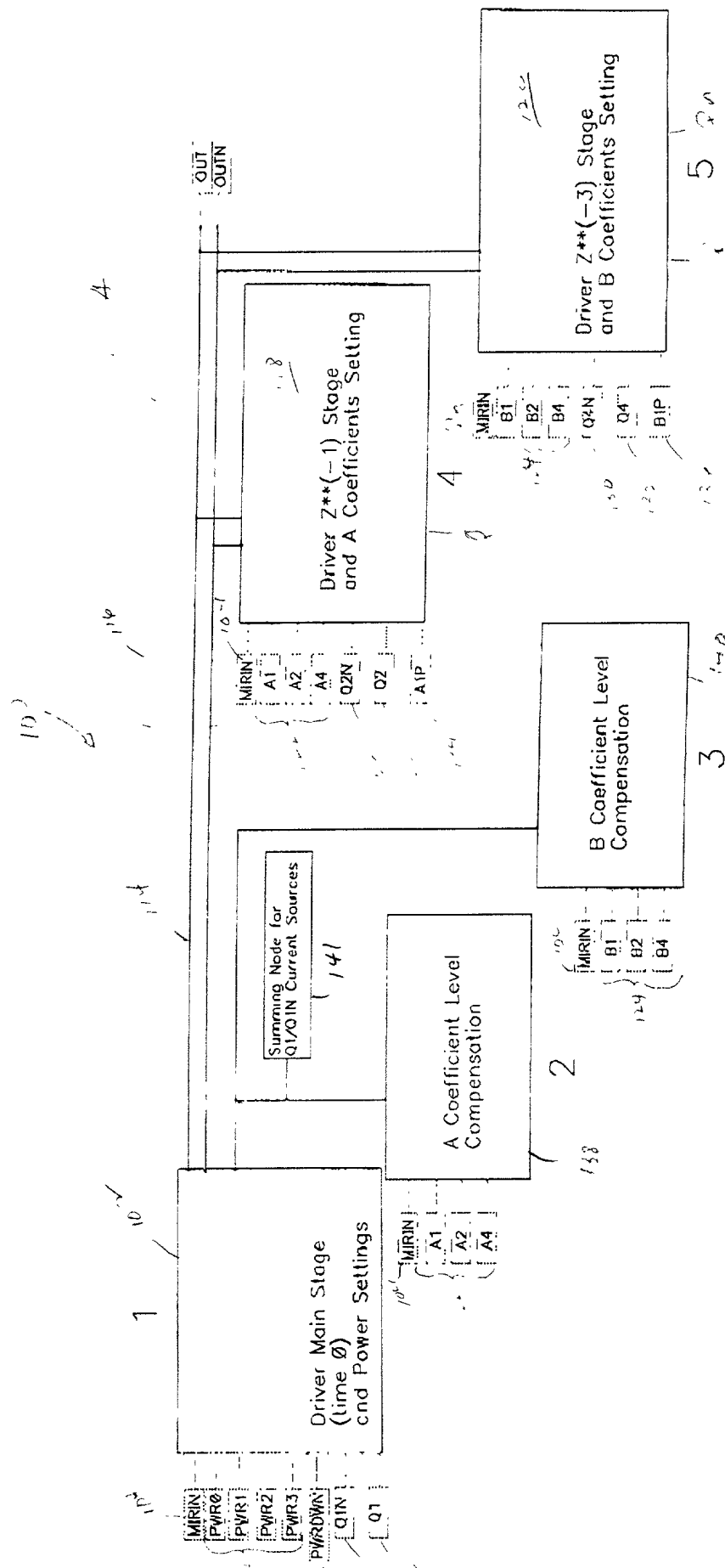
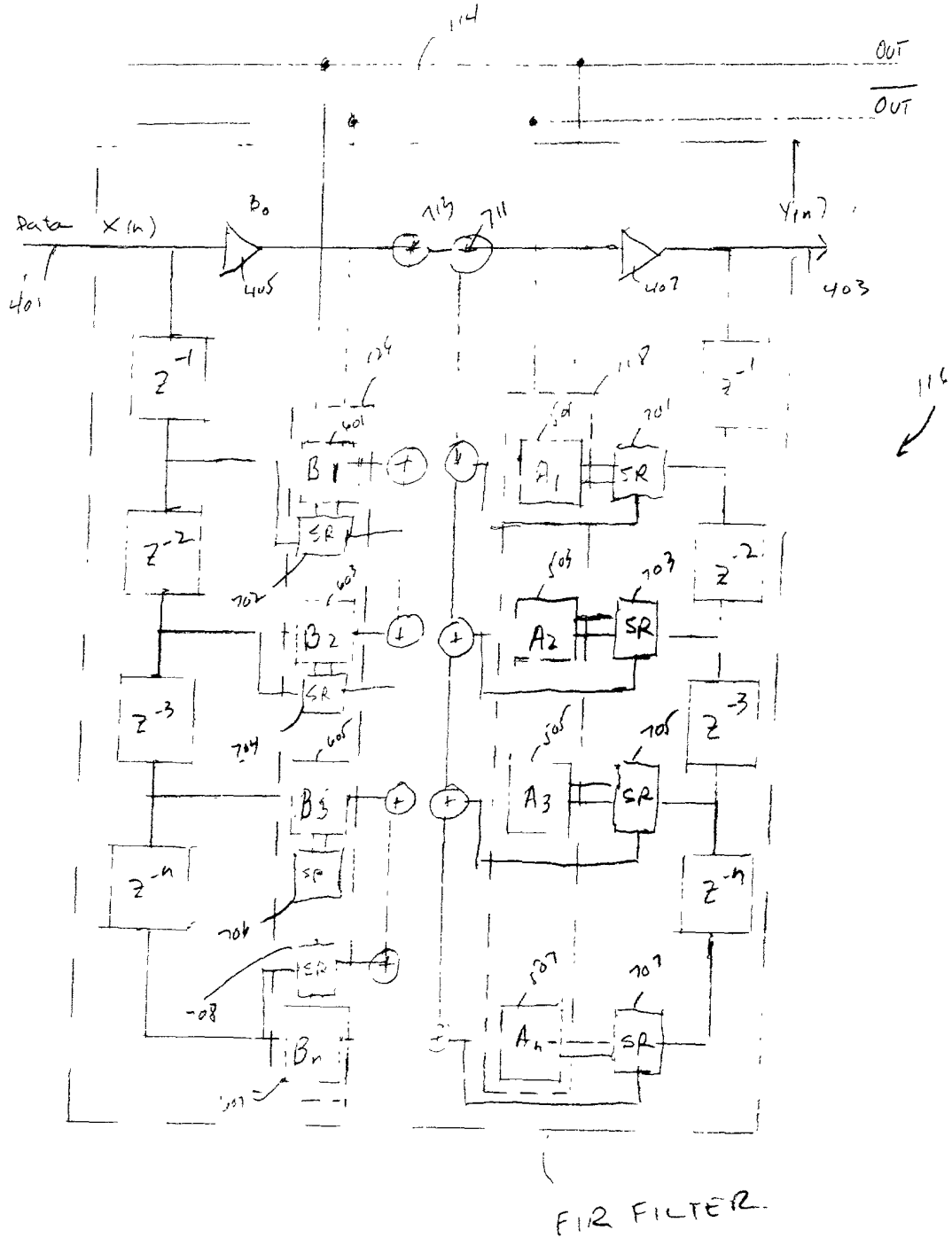


Figure 4



$$H(z) = AB_1 z^{-1} + AB_2 z^{-2} + AB_3 z^{-3}$$

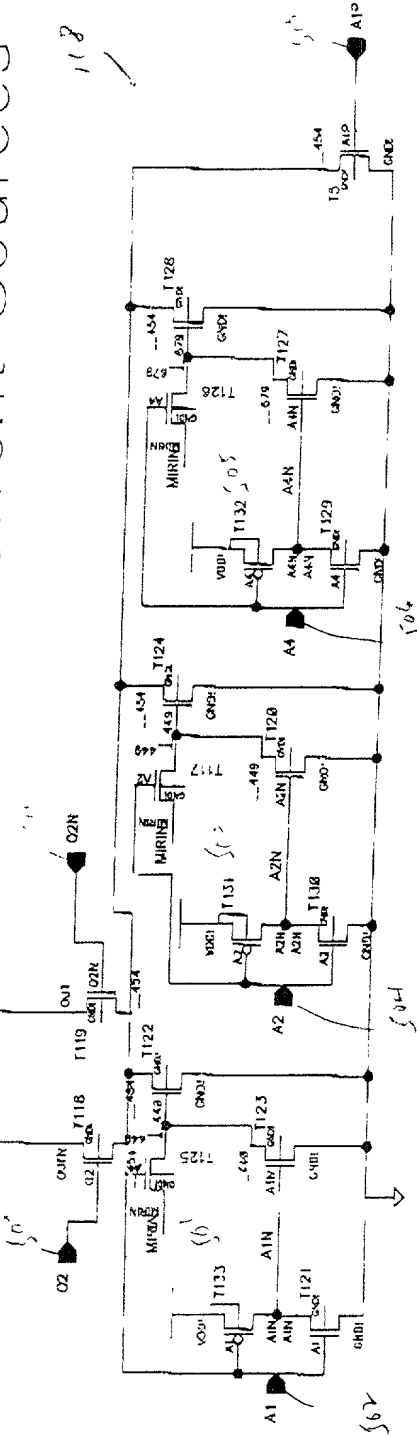
Figure 5

102 →

114

Summing Node for
Q2/Q2N Current Sources

118



A Coefficients
Setting

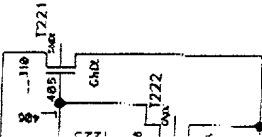
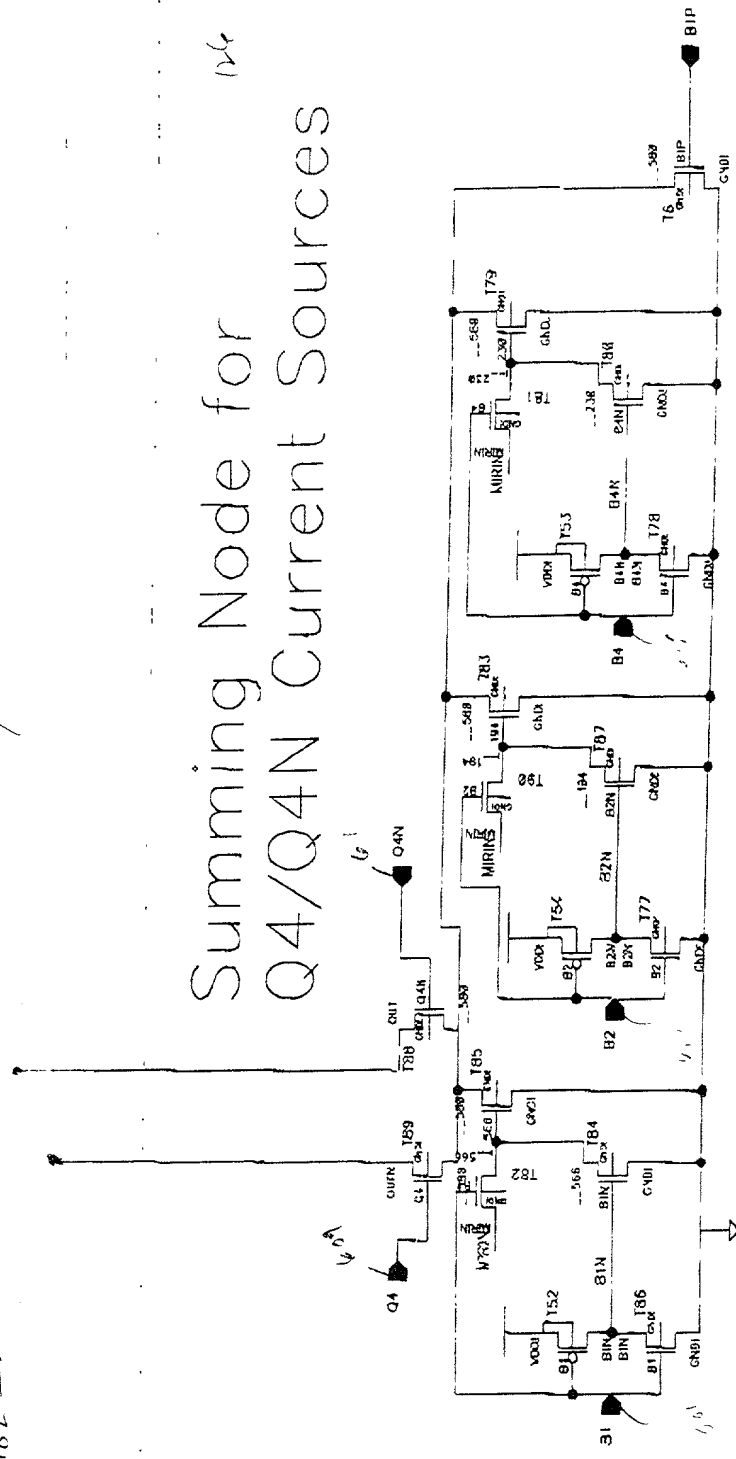


Figure 6

Summing Node for
Q4/Q4N Current Sources



B Coefficients
Setting

Figure 7

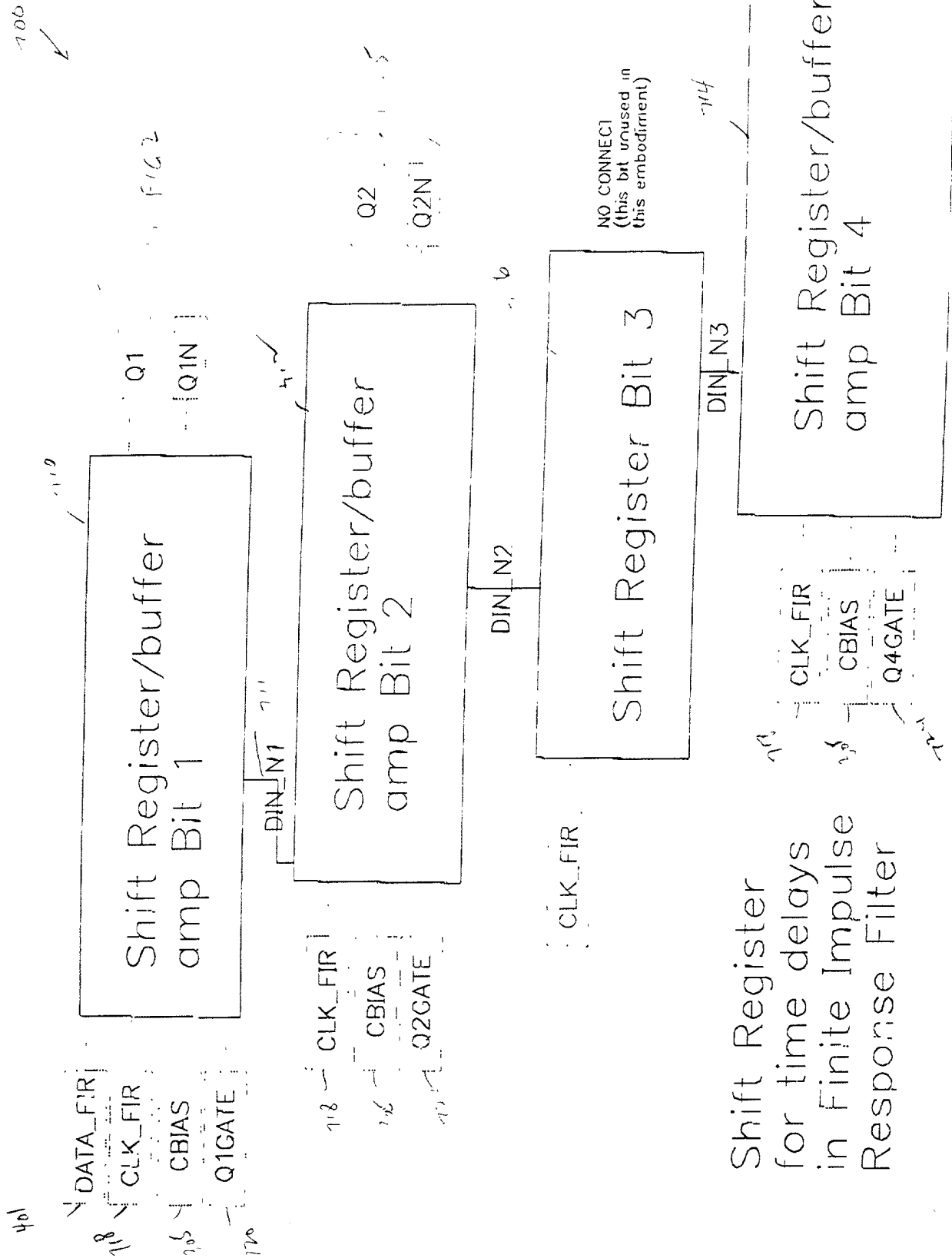
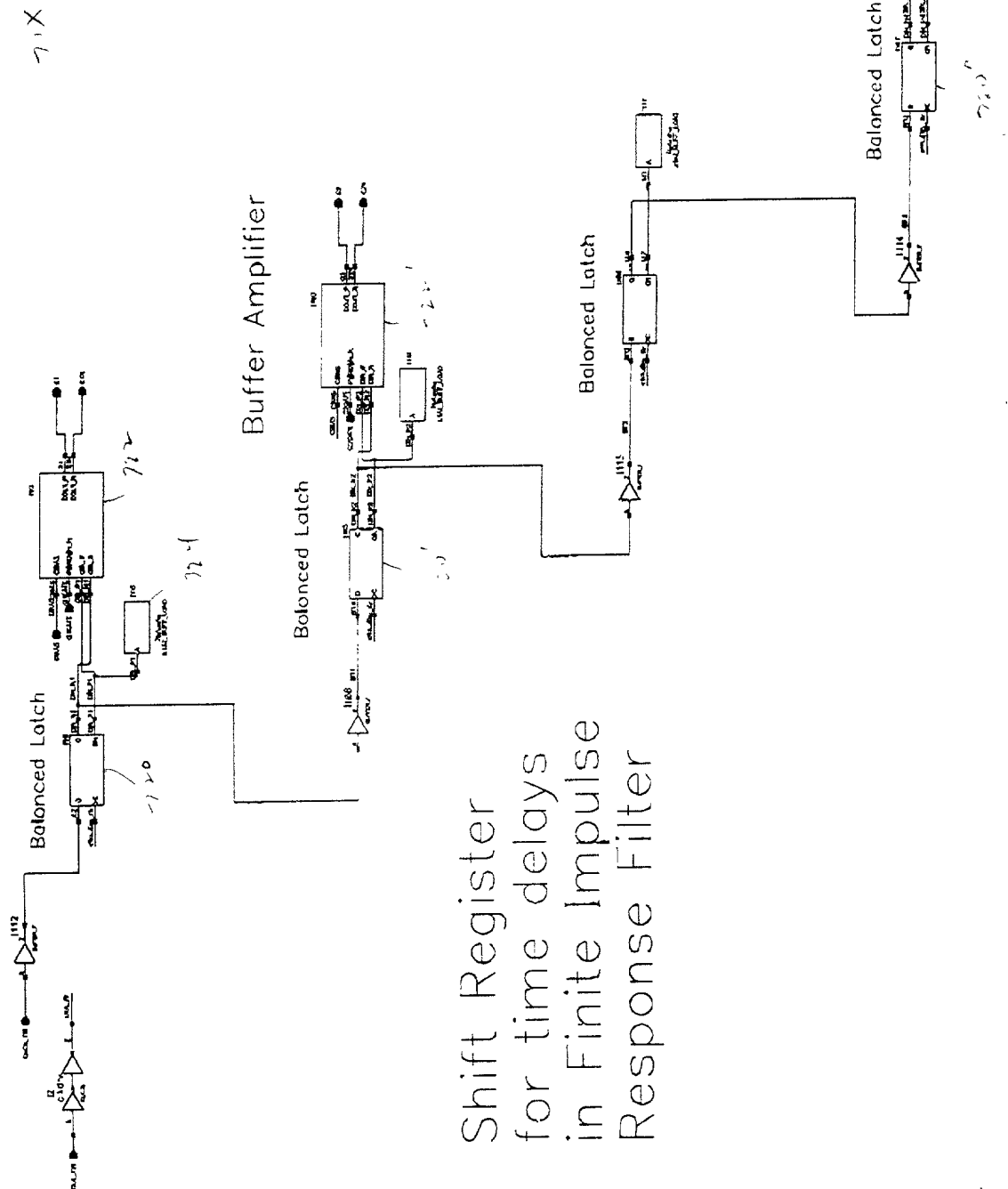


Figure 7A

Buffer Amplifier



Shift Register
for time delays
in Finite Impulse
Response Filter

Figure 7B

Balanced Latch

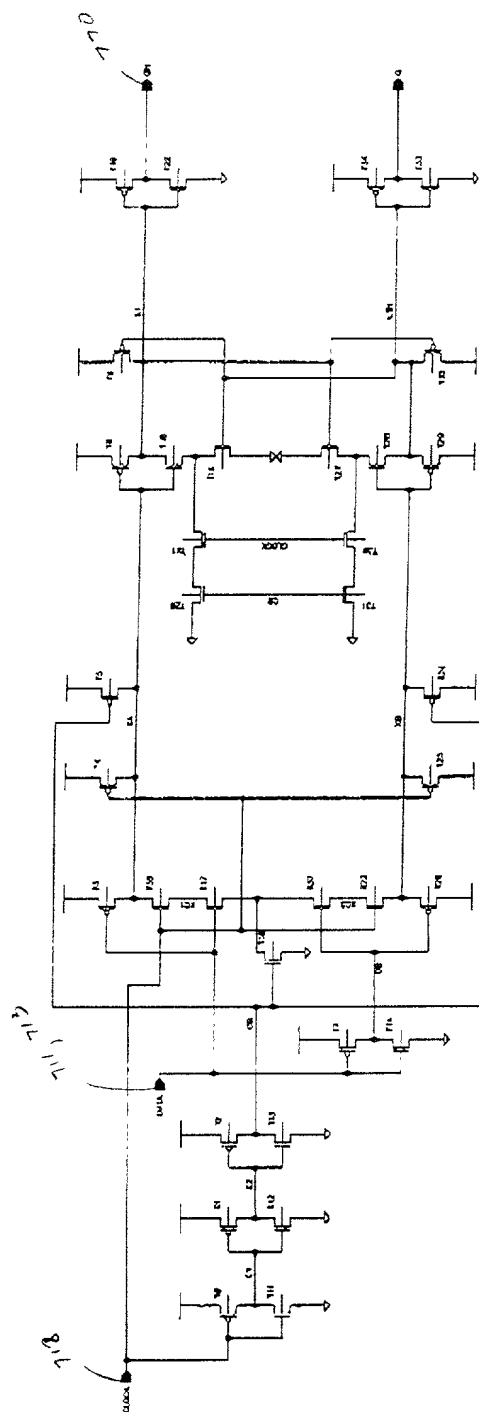


Figure 8

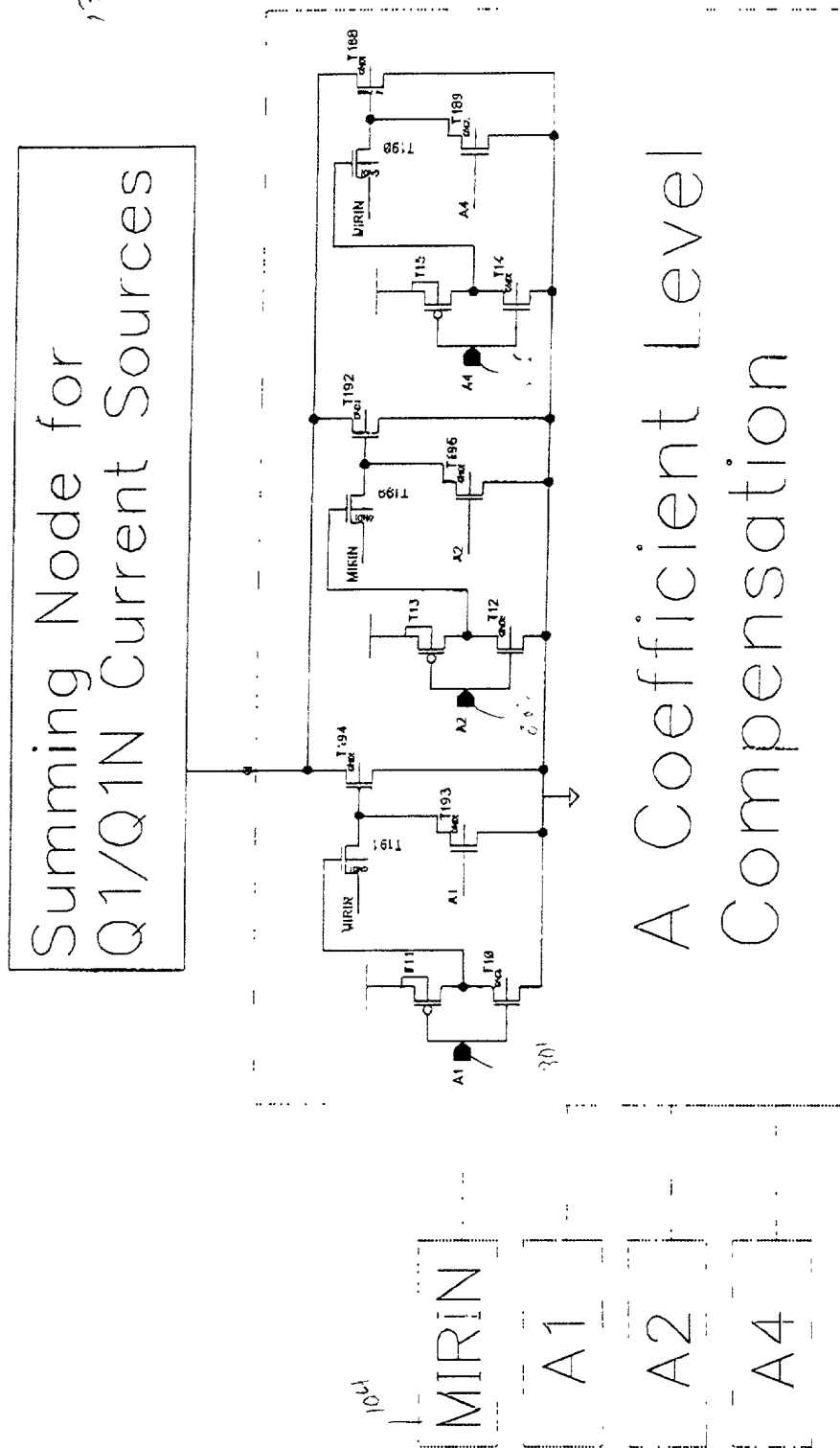
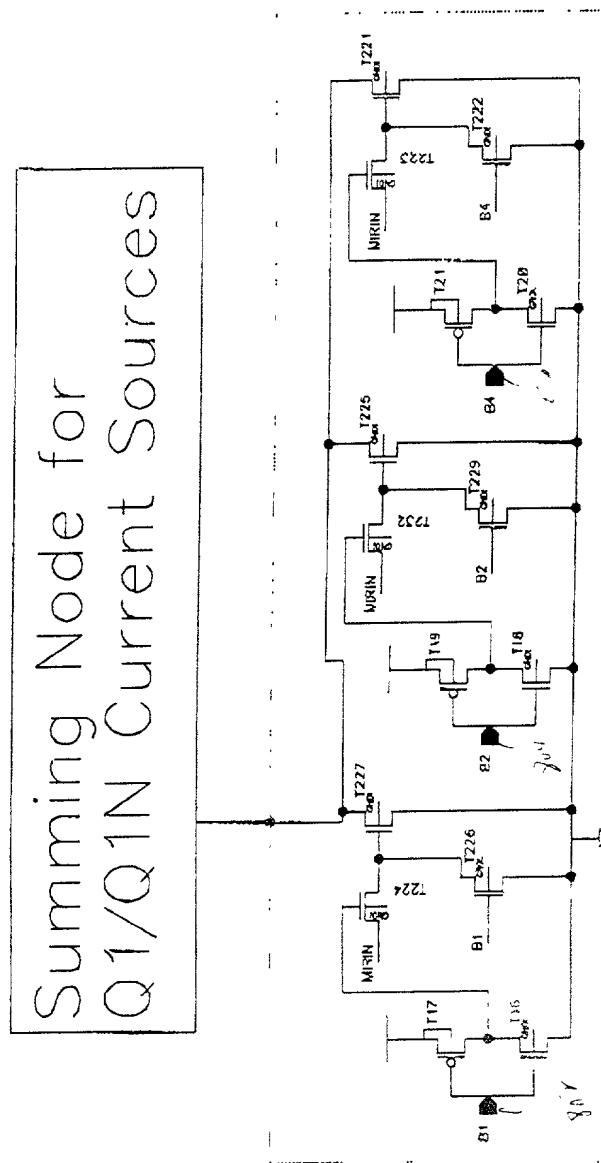


Figure 8A



MIRIN



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B Coefficient Level Compensation

Figure 9

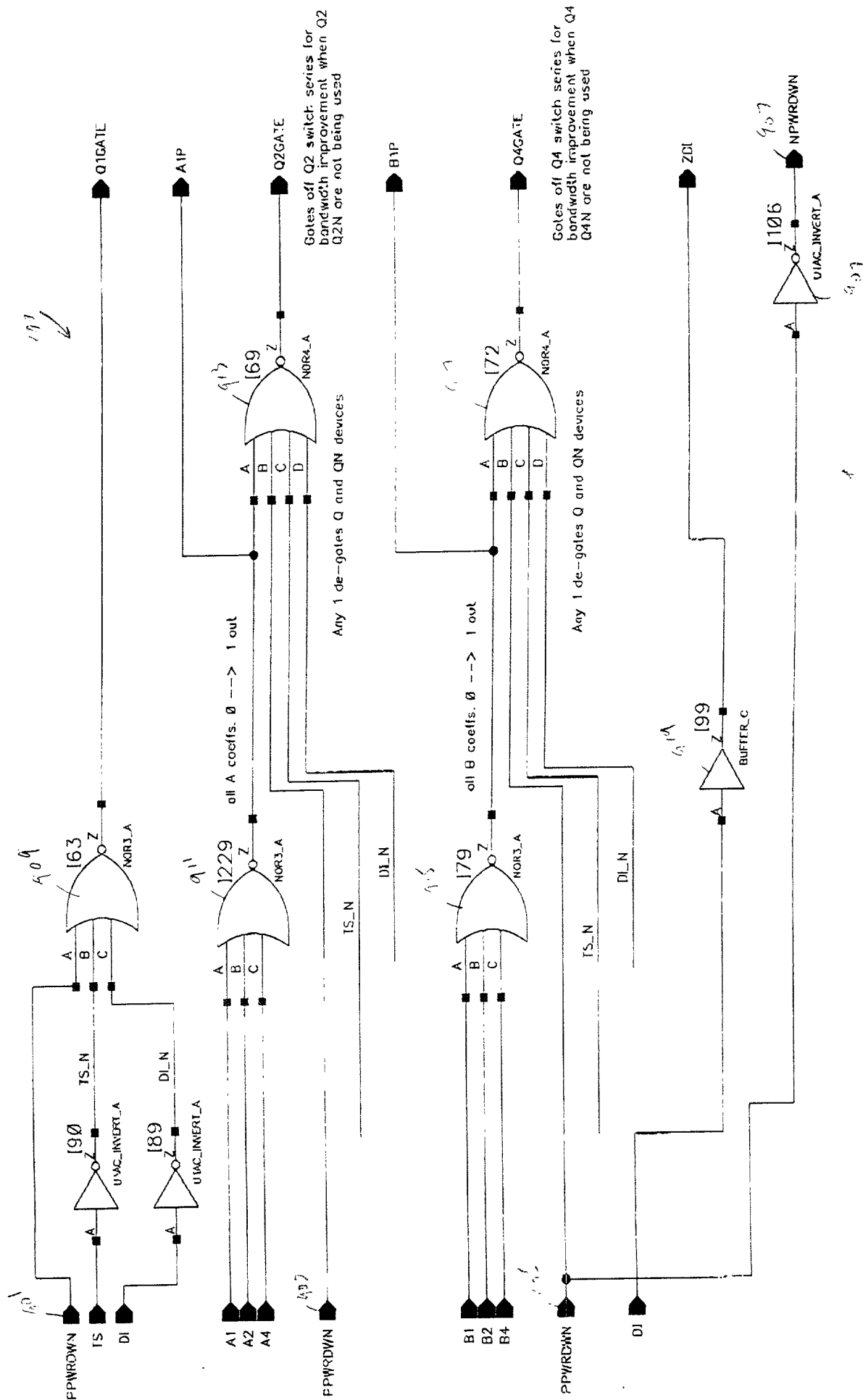
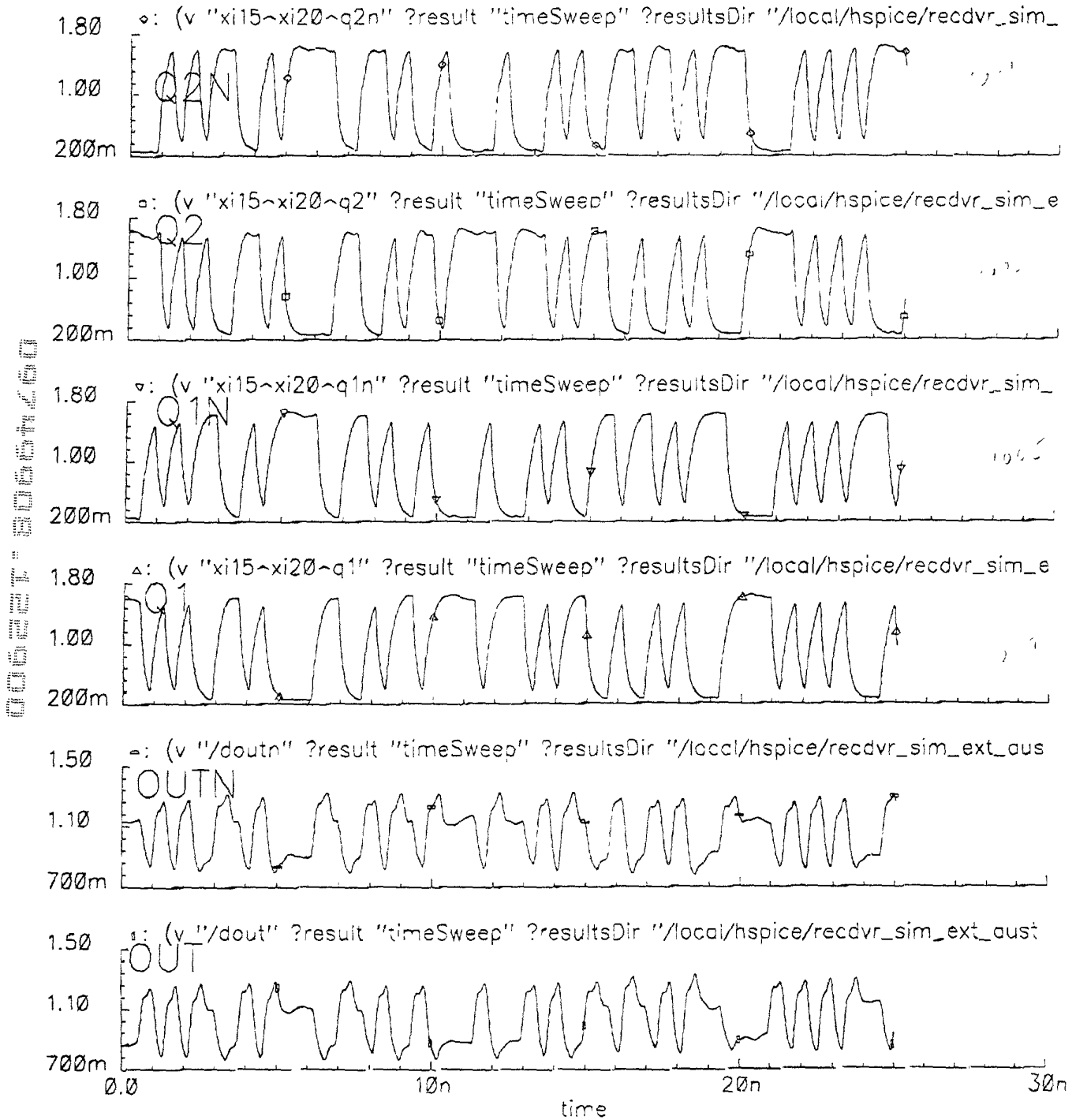


Figure 10



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Transient Response

